

STGB6NB60HD

N-CHANNEL 6A - 600V - D²PAK Low Drop PowerMESH™ IGBT

PRELIMINARY DATA

General features

Туре	V _{CES}	V _{CE(sat)} (Max)@ 25°C	l _C @100°C
STGB6NB60HD	600V	< 2.7V	6A

- LOWER C_{RES} / C_{IES} RATIO (NO CROSS CONDUCTION SUSCEPTIBILITY)
- HIGH FREQUENCY OPERATION
- VERY SOFT ULTRA FAST RECOVERY ANTI PARALLEL DIODE
- TYPICAL SHORT CIRCUIT WITHSTAND TIME 5MICROS S-family, 4micro H-family
- CO-PACKAGE WITH TURBOSWITCH™ ANTIPARALLEL DIODE

Description

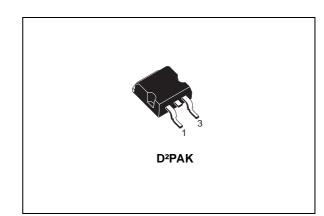
Using the latest high voltage technology based on a patented strip layout, STMicroelectronics has designed an advanced family of IGBTs, the PowerMESH[™] IGBTs, with outstanding performances. The suffix "H" identifies a family optimized for high frequency application.

Applications

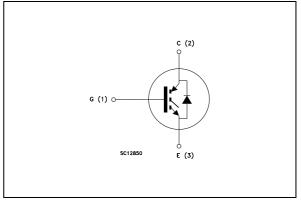
- HIGH FREQUENCY MOTOR CONTROL
- SMPS and PFC IN BOTH HARD SWITCH AND RESONANT TOPOLOGIES

Order codes

Sales Type	Marking	Package	Packaging
STGB6NB60HD	STGB6NB60HD GB6NB60HD		TAPE & REEL



Internal schematic diagram



November 2	2005
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1 Electrical ratings

Symbol	Parameter	Value	Unit	
V _{CES}	Collector-Emitter Voltage (V _{GS} = 0)	600	V	
۱ _C	Collector Current (continuous) at 25°C	12	А	
۱ _C	Collector Current (continuous) at 100°C	6	А	
I _{CM} Note 1	Collector Current (pulsed)	48	А	
V _{GE}	Gate-Emitter Voltage	± 20	V	
P _{TOT}	Total Dissipation at T _C = 25°C	80	W	
Тj	Operating Junction Temperature	– 65 to 150	0°	
T _{stg}	Storage Temperature	- 65 t0 150		

Table 1. Absolute maximum ratings

Table 2. Thermal resistance

		Min.	Тур.	Max.	Unit
Rthj-case	Thermal Resistance Junction-case			1.56	°C/W
Rthj-amb	Thermal Resistance Junction-ambient			62.5	°C/W
Rthc-h	Thermal Resistance case-hetsink		0.5		°C/W



2 Electrical characteristics

 $(T_{CASE} = 25 \ ^{\circ}C \text{ unless otherwise specified})$

Table 3. Static

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{BR(CES)}	Collectro-Emitter Breakdown Voltage	I _C = 250μA, V _{GE} = 0	600			V
V _{CE(SAT)}	Collector-Emitter Saturation Voltage	V _{GE} = 15V, I _C = 6A, Tj= 25°C V _{GE} = 15V, I _C = 6A, Tj= 125°C		2.1 1.6	2.7	V V
V _{GE(th)}	Gate Threshold Voltage	$V_{CE} = V_{GE}, I_C = 250 \mu A$	3		5	V
I _{CES}	Collector-Emitter Leakage Current (V _{GE} = 0)	V _{CE} = Max Rating,Tc=25°C V _{CE} = Max Rating, Tc=125°C			50 500	μΑ μΑ
I _{GES}	Gate-Emitter Leakage Current (V _{CE} = 0)	$V_{GE} = \pm 20V$, $V_{CE} = 0$			± 100	nA
9 _{fs}	Forward Transconductance	$V_{CE} = 25V_{,} I_{C} = 6A$	3	4.5		S

Table 4. Dynamic

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
C _{ies} C _{oes} C _{res}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	V _{CE} = 25V, f = 1MHz, V _{GE} = 0	390 45 10	560 68 15	730 90 20	pF pF pF
Q _g Q _{ge} Q _{gc}	Total Gate Charge Gate-Emitter Charge Gate-Collector Charge	V _{CE} = 480V, I _C = 6A, V _{GE} = 15V, (see Figure 2)		42 7.9 17.6	55	nC nC nC
I _{CL}	Turn-Off SOA Minimum Current	$V_{clamp} = 480V$, Tj = 150°C R _G = 10 Ω , V _{GE} = 15V	52			A



Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
t _{d(on)} t _r (di/dt) _{on}	Turn-on Delay Time Current Rise Time Turn-on Current Slope	$V_{CC} = 480$ V, $I_C = 6$ A R _G = 10 Ω , V_{GE} = 15V, Tj= 125°C (see Figure 3)		15 48 160		ns ns A/µs
$\begin{matrix} t_{c} \\ t_{f}(V_{off}) \\ t_{d}(_{off}) \\ t_{f} \end{matrix}$	Cross-over Time Off Voltage Rise Time Turn-off Delay Time Current Fall Time	V_{cc} =480V, I _C = 6A, R _{GE} =10 Ω , V _{GE} =15V, Tj=25°C (see Figure 3)		85 20 75 70		ns ns ns
t _c t _r (V _{off}) t _d (_{off}) t _f	Cross-over Time Off Voltage Rise Time Turn-off Delay Time Current Fall Time	V_{cc} =480V, I _C = 6A, R _{GE} =10 Ω , V _{GE} =15V, Tj=125°C (see Figure 3)		150 50 110 110		ns ns ns

Table 5. Switching on/off (inductive load)

Table 6. Switching energy (inductive load)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
	Turn-on Switching Losses Turn-off Switching Losses Total Switching Losses	$V_{cc} = 480V, I_C = 6A,$ $R_{GE} = 10\Omega, V_{GE} = 15V, Tj = 25°C$ (see Figure 3)		150 85 235		μJ μJ
	Turn-on Switching Losses Turn-off Switching Losses Total Switching Losses	$V_{cc} = 480V, I_C = 6A,$ $R_{GE} = 10\Omega, V_{GE} = 15V, Tj = 125°C$ (see Figure 3)		185 220 405		μ Γ Π

 Table 7.
 Collector-emitter diode

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _f	Forward On-Voltage	lf = 6A lf = 6A, Tj = 125°C		1.8 1.4	2.2	V V
l _f I _{fm}	Forward Current Forward Current pulsed				6 48	A A
t _{rr} Q _{rr} I _{rrm}	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current	If = 6A, V_R = 200V, T _j = 125°C, di/dt = 100A/µs (see Figure 4)		100 135 2.7		ns ns nC

(1)Pulse width limited by max. junction temperature

(2) Eon is the tun-on losses when a typical diode is used in the test circuit in figure 2 Eon include diode recovery energy. If the IGBT is offered in a package with a co-pak diode, the co-pack diode is used as external diode. IGBTs & Diode are at the same temperature (25°C and 125°C)

(3) Turn-off losses include also the tail of the collector current



3 Test Circuits

Figure 1. Test Circuit for Inductive Load Switching

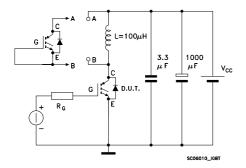
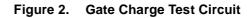
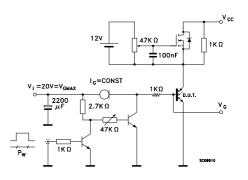
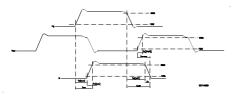


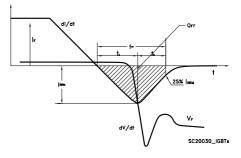
Figure 3. Switching Waveform













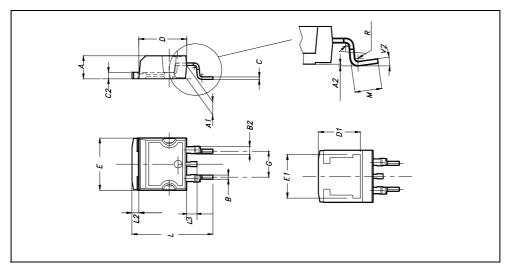
4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

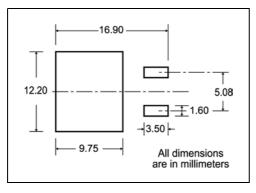


DIM.		mm.		inch		
DINI.	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
А	4.4		4.6	0.173		0.181
A1	2.49		2.69	0.098		0.106
A2	0.03		0.23	0.001		0.009
В	0.7		0.93	0.027		0.036
B2	1.14		1.7	0.044		0.067
С	0.45		0.6	0.017		0.023
C2	1.23		1.36	0.048		0.053
D	8.95		9.35	0.352		0.368
D1		8			0.315	
E	10		10.4	0.393		
E1		8.5			0.334	
G	4.88		5.28	0.192		0.208
L	15		15.85	0.590		0.625
L2	1.27		1.4	0.050		0.055
L3	1.4		1.75	0.055		0.068
М	2.4		3.2	0.094		0.126
R		0.4			0.015	
V2	0°		4º			

D²PAK MECHANICAL DATA

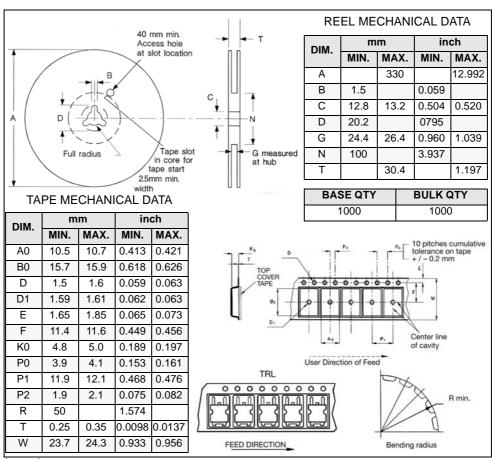


5 Packaging mechanical data



D²PAK FOOTPRINT

TAPE AND REEL SHIPMENT



* on sales type

6 Revision History

Date	Revision	Changes
18-Nov-2005	1	Initial release.



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